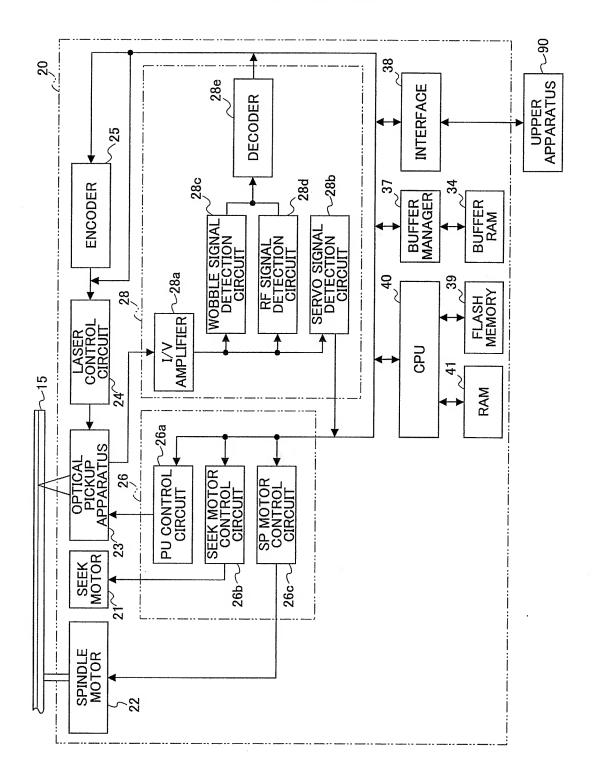
App No.: 10/589,895 Inventor: Yoshiyuki Sasaki Docket No.: R2184.0527/P527

Title: DEFECT MANAGEMENT INFORMATION SETTING

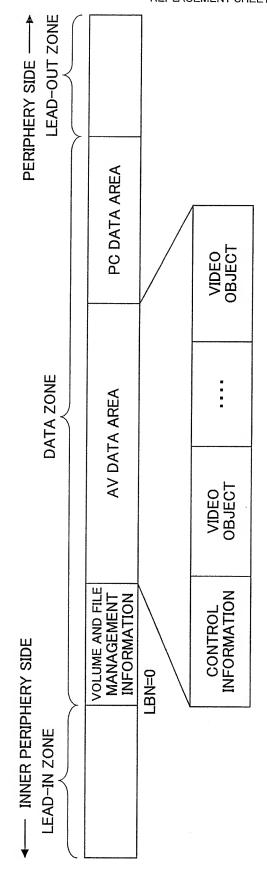
METHOD, RECORDING METHOD, DEFECT, etc.

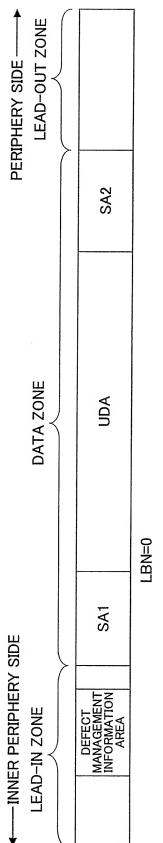
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-1G.1

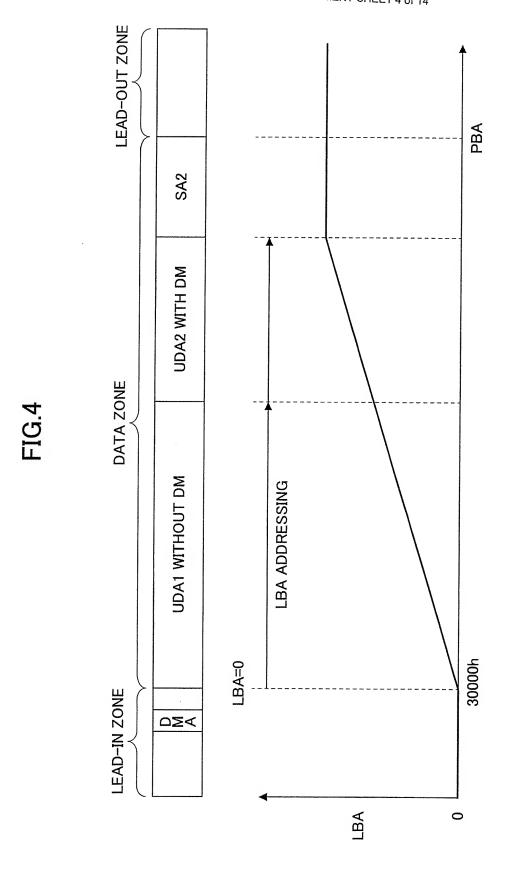
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BP in Block	Contents	Length in bytes
0	Signature of the DMI ("DMI")	က
3	Version number	-
4	DMI update count	4
8	UDA1 start address pointer	4
12	UDA1 end address pointer	4
16	UDA2 start address pointer	4
20	UDA2 end address pointer	4
24	SA2 size	4
28	Number of Replacement List (RPL) entries = N	4
32	RPL entry 0	8
40	RPL entry 1	8
$(N-1) \times 8+32$	RPL entry N	8

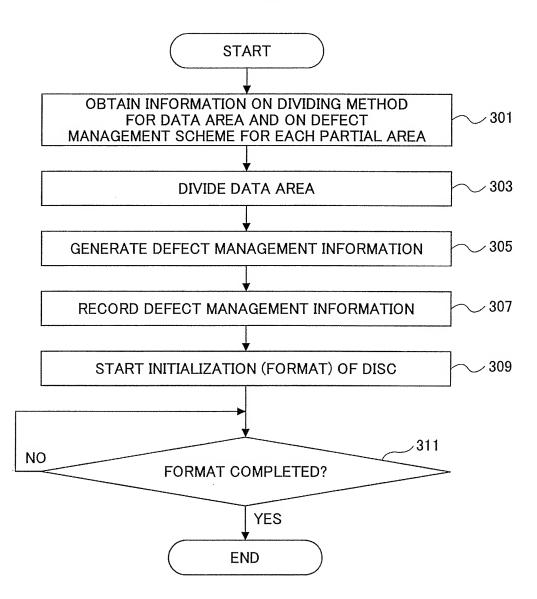
App No.: 10/589,895 Inventor: Yoshiyuki Sasaki

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FIG.6



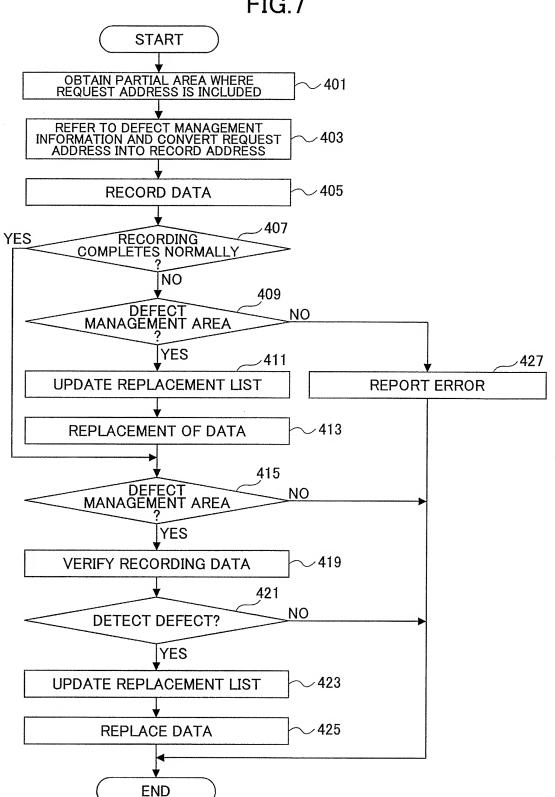
App No.: 10/589,895

Inventor: Yoshiyuki Sasaki

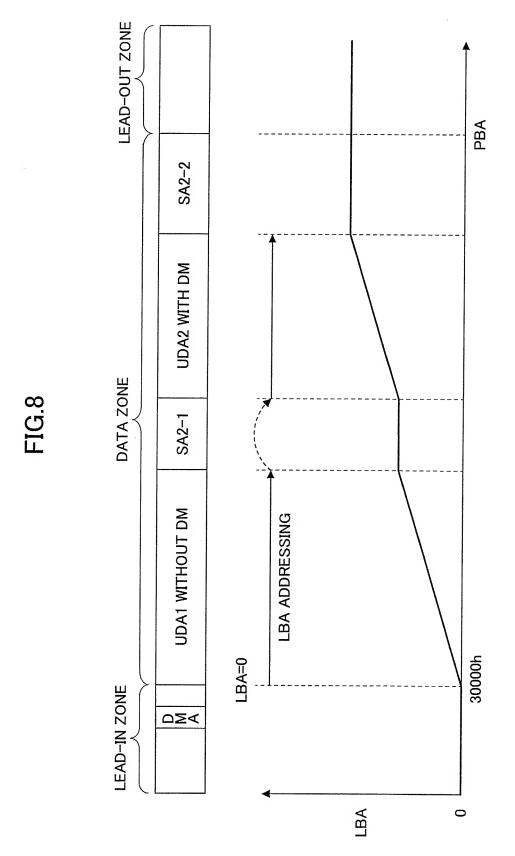
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Length in bytes	က	-	4	2	9	32	32	8	8	8
Contents	Signature of the DMI ("DMI")	Version number	DMI update count	Number of RPL Blocks = 2	Reserved	RPL Block 1	RPL Block 2	RPL2 entry 0	RPL2 entry 1	 RPL2 entry N
BP in Block	0	3	4	8	10	16	48	80	88	$(N-1) \times 8+80$

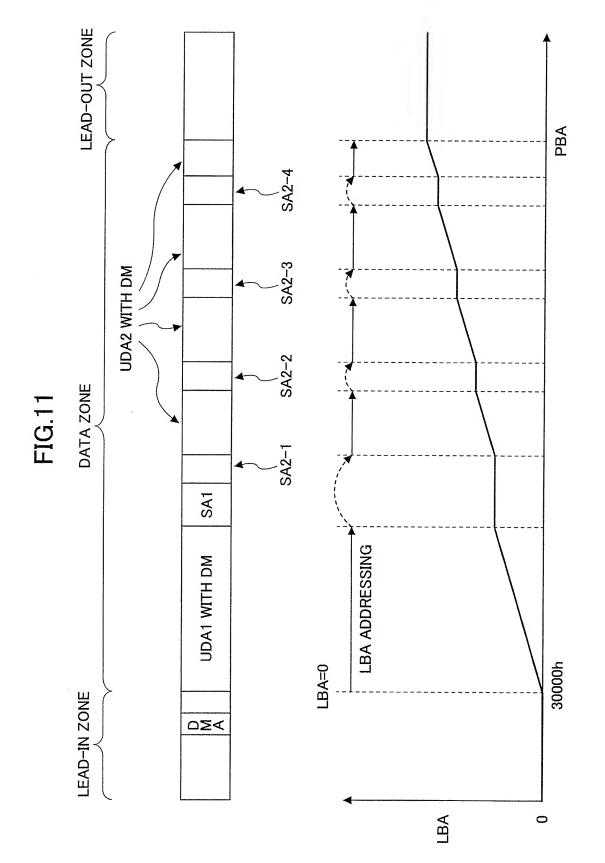
App No.: 10/589,895 Docket No.: R2184.0527 Inventor: Yoshiyuki Sasaki Title: DEFECT MANAGEMENT INFORMATION SETTING METHOD, RECORDING METHOD, DEFECT, etc. REPLACEMENT SHEET 10 of 14

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BP in Block	Contents	Length in bytes
0	Signature of the RPL Block ("RPL")	က
3	RPL Block number	The state of the s
4	UDA start address pointer	4
8	UDA end address pointer	4
12	SA-1 size	4
16	SA-2 size	4
20	Number of RPL entries	4
24	Location of RPL entry 0	2
26	Reserved	9

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BP in Block	Contents	Length in bytes
0	Signature of the DMI ("DMI")	3
8	Version number	
4	DMI update count	4
8	Number of RPL Blocks = 2	2
10	Reserved	9
16	RPL Block 1	64
80	RPL Block 2	64
144	RPL1 entry 0	8
152	RPL1 entry 1	8
$(N-1) \times 8+144$	RPL1 entry N1	8
$(N-1) \times 8+8+144$	RPL2 entry 0	8
(N-1) × 8+16+144	RPL2 entry 1	8
	••••	
$(N1-1) \times 8+(N2-1) \times 8+144$	RPL2 entry N2	8

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BP in Block	Contents	Length in bytes
0	Signature of the RPL Block ("RPL")	က
3	RPL Block number	-
4	RPL Block type	-
5	Reserved	3
8	UDA start address pointer	4
12	UDA end address pointer	4
16	Number of Replacement List entries	4
20	Location of RPL entry 0	2
22	Reserved	10
32	RPL Block specific data	32

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